# MOSCAD

### **Preliminary Specifications**

## VR3\_12\_1 - 3.3 to 1.2V Voltage regulator

#### **FEATURES**

1.2V output ± 10%

1mA maximum Load current

Maximum load regulation drop of 30mV

Maximum quiscent current of 1.8uA

Vin = 1.6V...3.6V

External capacitor 1uF or higher (Ceramic)

#### **APPLICATIONS**

On chip voltage regulation for analog or digital circuits

TECHNOLOGY: 0.13um Low Leakage CMOS

#### **OVERVIEW**

The cell is a low power, series 3.3V to 1.2 V voltage regulator. The maximum internal current, does not exceed 1.8uA. The cell also shows excellent line- and load regulation characteristics. Its low quiscent current makes the cell ideally suited for battery operated equipment. The cell includes an ultra-low-power bandgap reeference circuit.

#### **FUNCTIONAL DIAGRAM**

